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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Low, *et al.* Docket No.: 02 P 15173 US
Serial No.: 10/689,233 Art Unit: 2829
Filed: October 20, 2003 Examiner: Asok K. Sarkar
Title: Inclusion Of Low-K Dielectric Material Between Bit Lines

Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Prior to examination on the merits, Applicant respectfully submits this Preliminary
Amendment as set forth below: